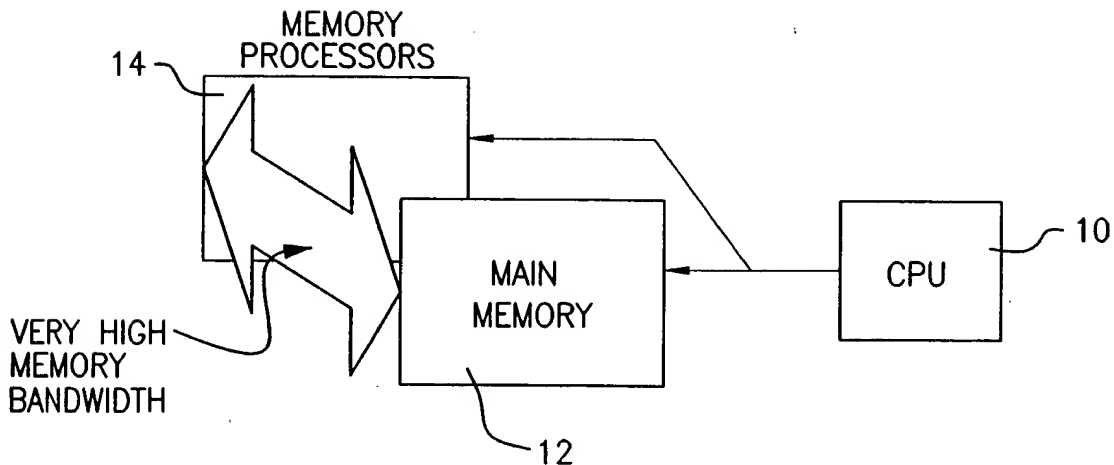


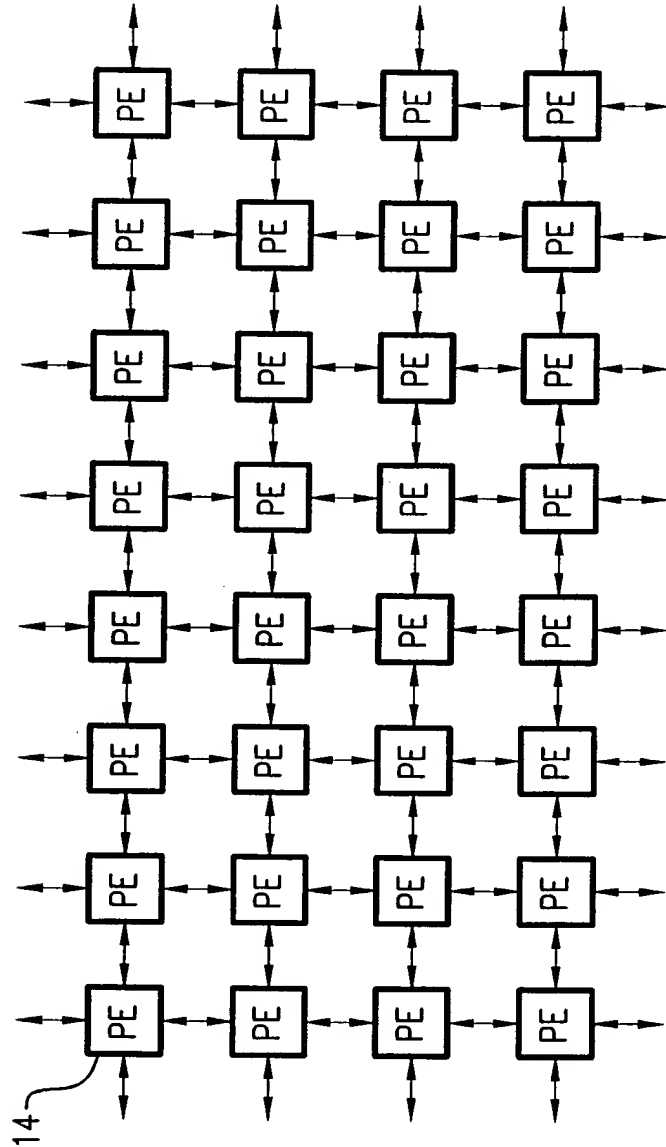
**FIG. 1**  
PRIOR ART



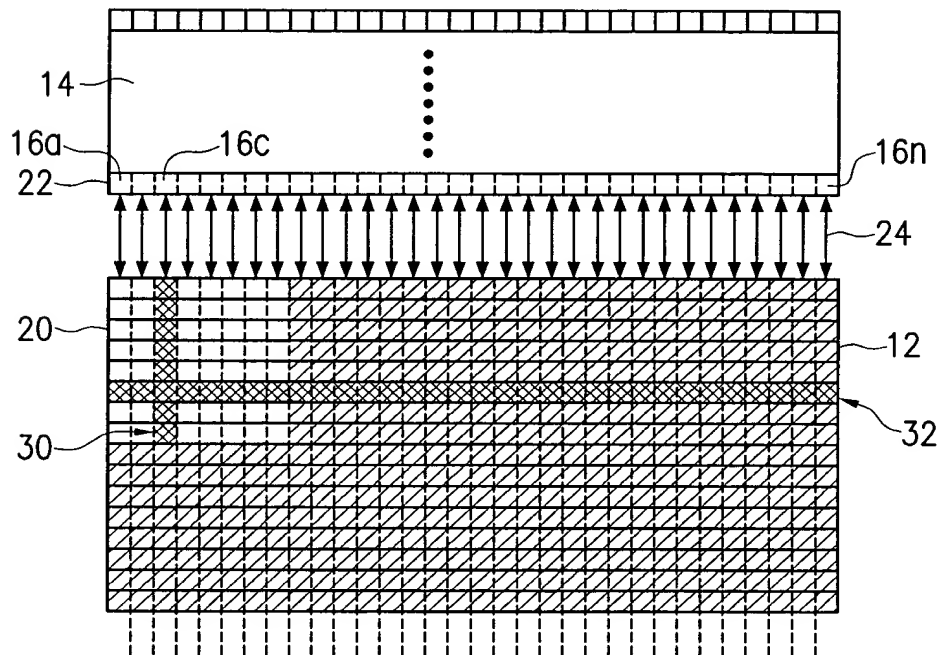
**FIG. 2**  
PRIOR ART

Inventor: Graham Kirsch

Title: METHOD AND APPARATUS FOR CONNECTING A MASSIVELY  
PARALLEL PROCESSOR ARRAY TO A MEMORY ARRAY IN A BIT  
SERIAL MANNER  
REPLACEMENT SHEET

*FIG. 3*

PRIOR ART

**FIG. 4**

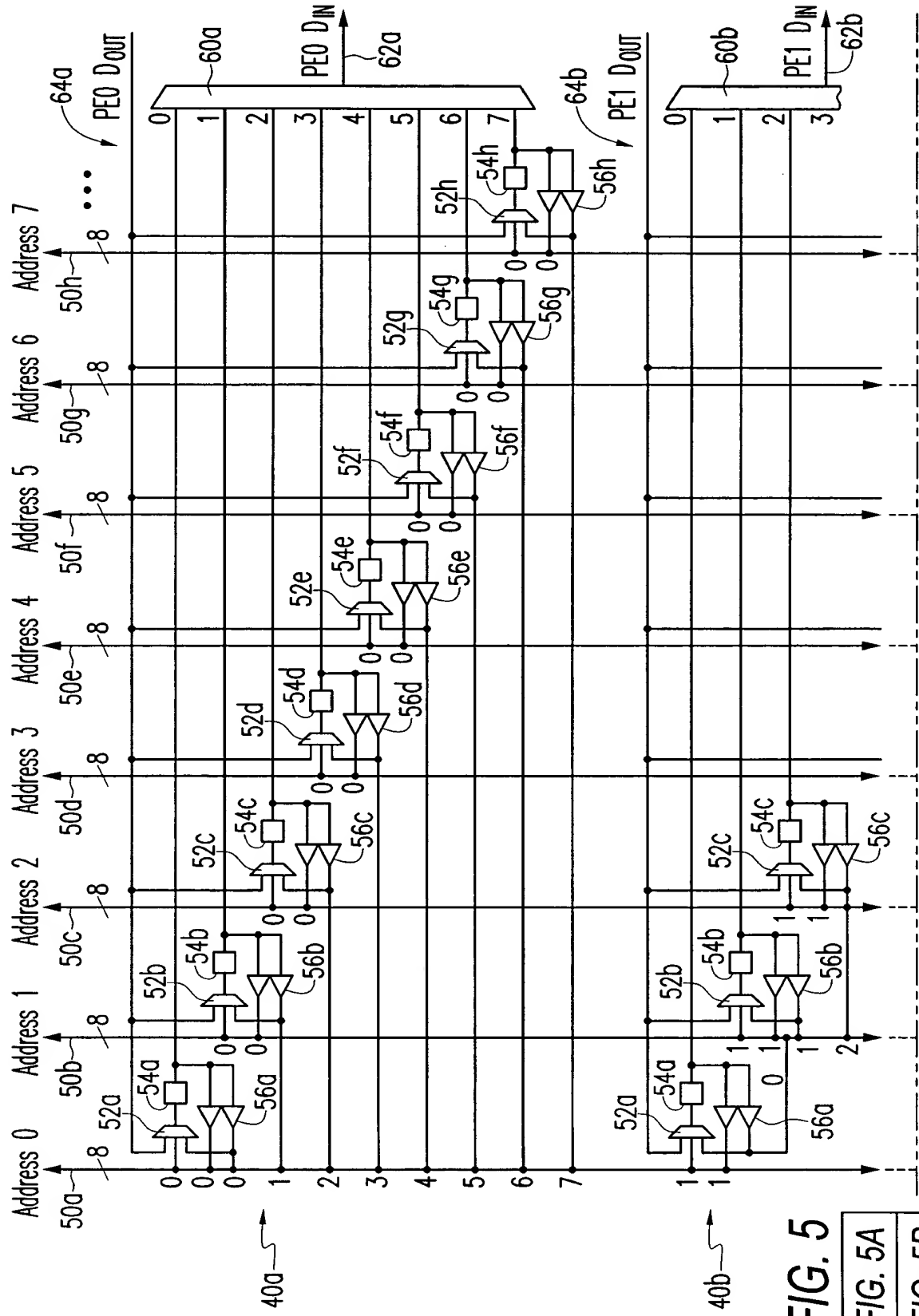
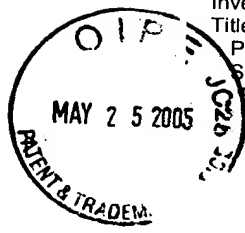


FIG. 5A

FIG. 5

FIG. 5A

FIG. 5B

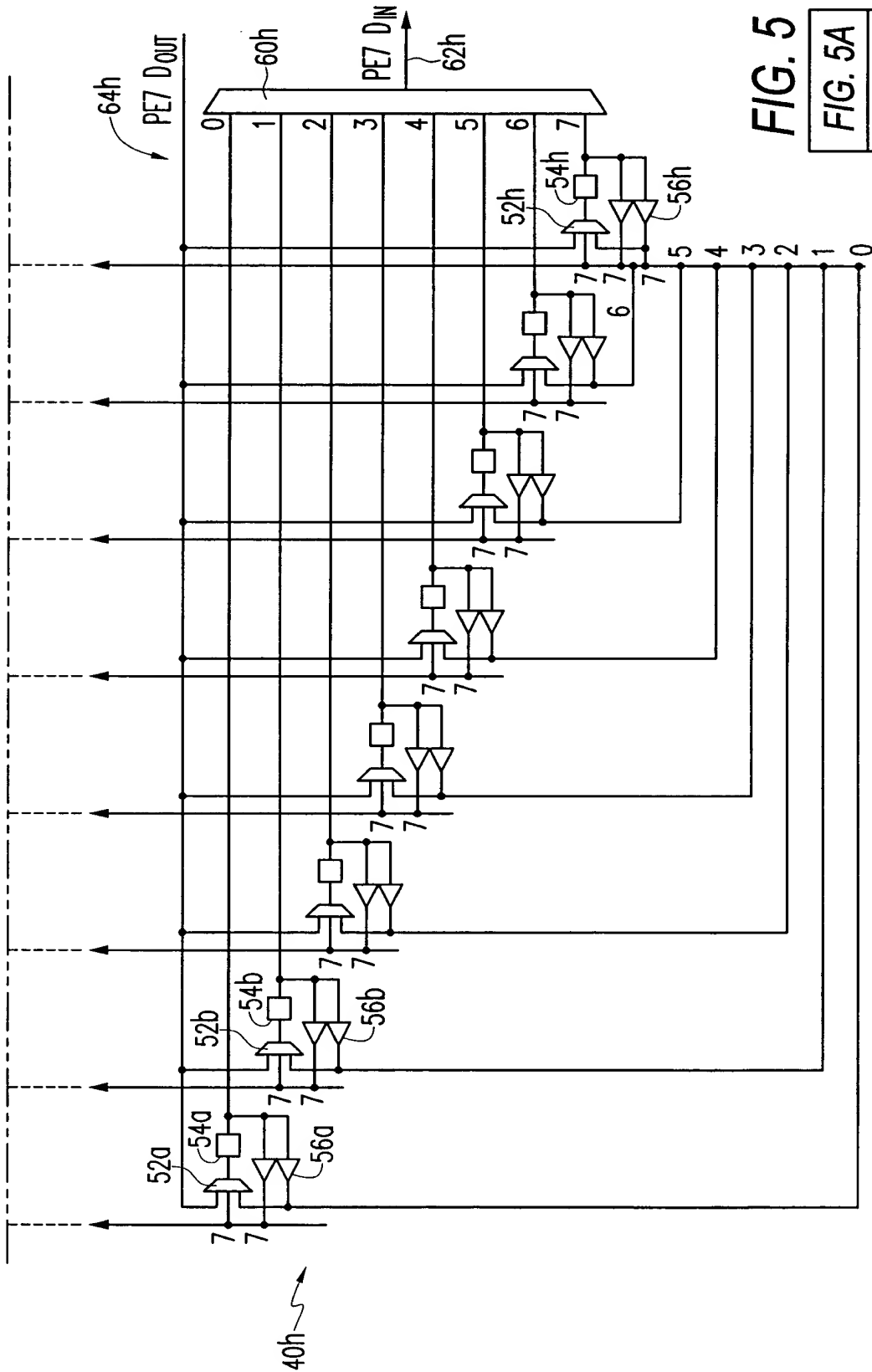
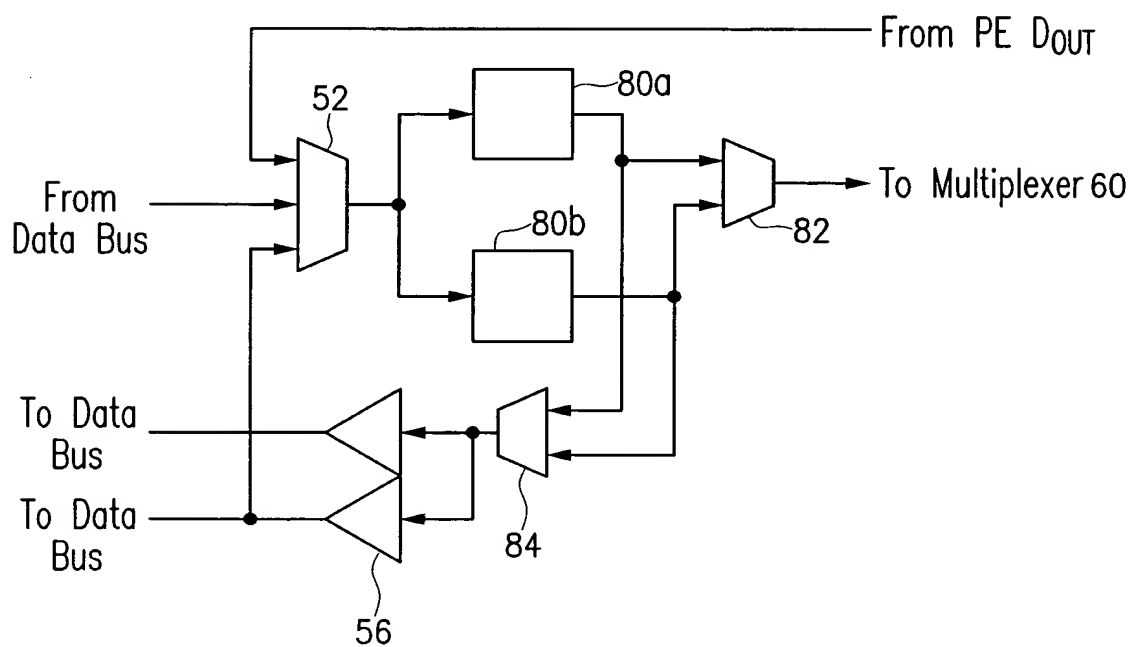
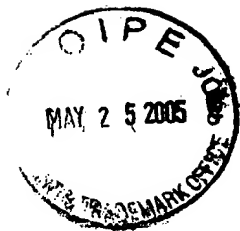


FIG. 5

FIG. 5A

FIG. 5B

FIG. 5B

**FIG. 6**

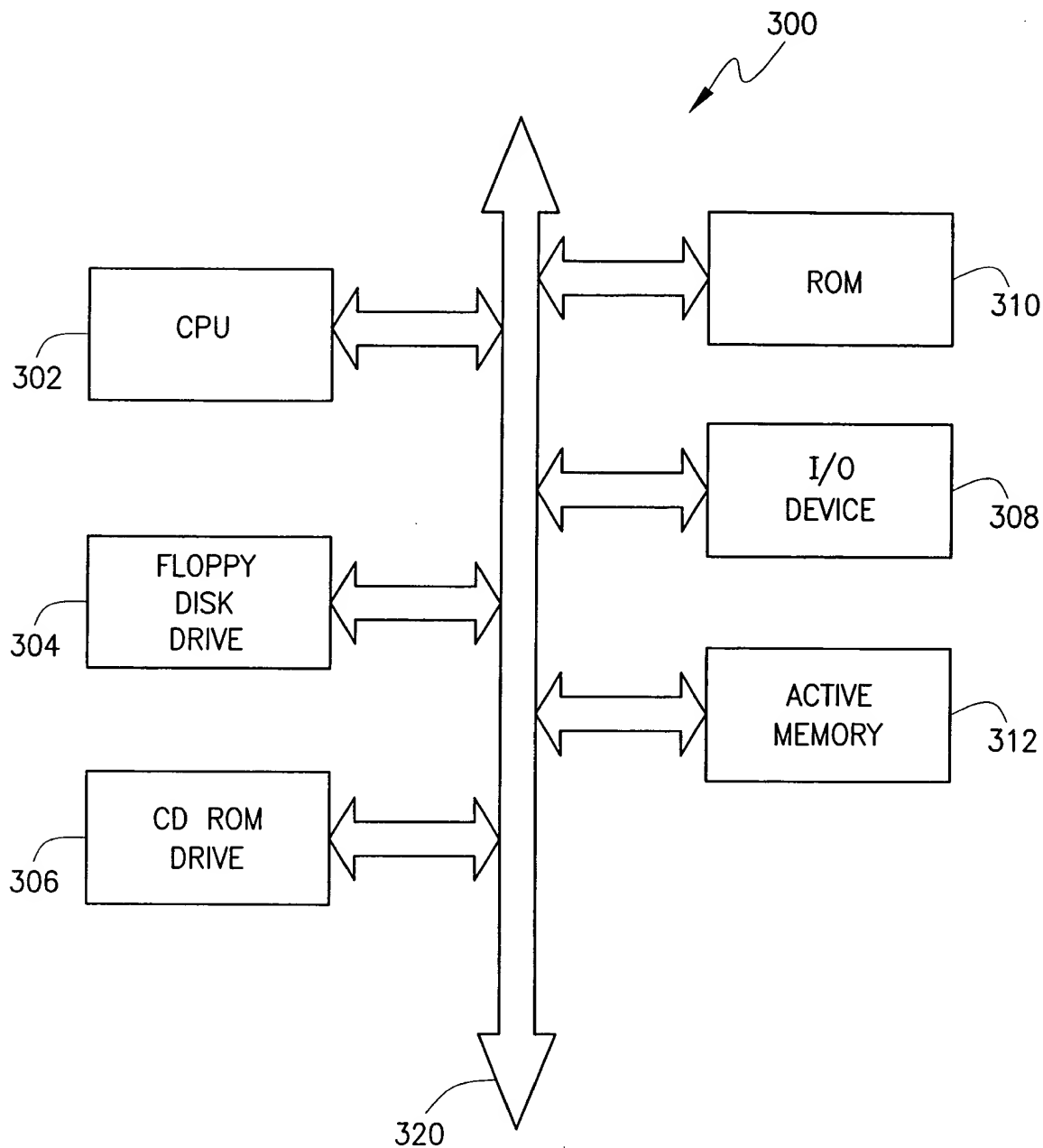


FIG. 7